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Computer science - Architecture and organization | Britannica

where the problem size is increased with the machine size so that the solution time is the same for sequential and parallel executions. 2.6 a. Say Program P1 consists of n x86 instructions, and hence 1.5 x n MIPS instructions. Computer A operates at 2.5 GHz, i.e. it takes 0.4ns per clock. So

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